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CONFIRMATION NO. 6522

SERIAL NUMBER 10/665,927	FILING DATE 09/17/2003 RULE	CLASS 716		GROUP ART UNIT 2825		JNIT	ATTORNEY DOCKET NO. 02-5799 81604		
APPLICANTS									
Alexander Tetelbaum, Hayward, CA;									
** CONTINUING DATA **********************************									
** FOREIGN APPLICATIONS ************************************									
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 12/11/2003									
Foreign Priority claimed 35 USC 119 (a-d) conditions met yes no Met after Allowance Verified and Acknowledged Examiner's Signa ure Initials			STATE OR	SHEETS		TOTAL		INDEPENDENT	
			COUNTRY CA	DRA	DRAWING 10		AIMS 27	CLAIMS 3	
ADDRESS Leo J. Peters LSI Logic Corporation 1551 McCarthy Blvd., MS D-106 Milpitas , CA 95035									
. TITLE Method of noise analysis and correction of noise violations for an integrated circuit design									
				☐ All Fees					
FILING FEE FEE	: Authority has been given			1.16 Fees (Filing)					
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